



QUINTESSENT

JOB OPENING: Senior Photonic Engineer

About us:

Modern computing applications such as large-scale A.I. are bottlenecked by the available data movement bandwidth. The computing infrastructure needed in the future will be even more bandwidth starved due to the pace of growth and proliferation of such applications. Quintessent is developing future-proof connectivity solutions to solve the data movement bottleneck. Our team comprises of technology pioneers and serial entrepreneurs with a long track record of entrepreneurial success at multiple past ventures. We are seeking talented and adventurous individuals to join us on our journey as fellow members of a stellar team.

Employment type: Full time

Responsibilities:

The ideal candidate will be comfortable as both a hands on contributor as well as a technical authority in their domain of expertise. Primary areas of responsibilities may include any or all of the following depending on experience and fit: design, simulation, mask layout, test, and analysis of photonic integrated circuits (PICs) and devices from early concept demonstrations to volume product prototyping/manufacturing. The candidate will be working closely with other members of the Engineering team to drive technology development and help shape commercial solutions instantiated from our technology.

Qualifications:

- Ph.D. degree in Physics, Applied Physics, or Electrical Engineering
- 5+ years of experience with III-V and/or silicon photonic device design, simulation, fabrication, and/or testing in a commercial environment.
- Experience with mask layout software and process design kits (PDK), either custom developed in house or used with commercial foundries.
- Experience with simulation methods/software such as film mode matching (FMM), finite difference time domain (FDTD), and beam propagation
- Experience with computer programming such as Python. Experience with programmatic layout is strongly preferred.
- Experience working with commercial foundries for device and/or PIC fabrication.
- Familiarity with PIC design tradeoffs for multiple devices, link budget analysis, and optical signal integrity considerations.
- Effective communicator and collaborator.
- Fast learner and problem solver.
- Meticulous attention to detail and consistency in execution.

For more questions or to apply for the position, contact: [hiring@quintessent.com](mailto: hiring@quintessent.com)